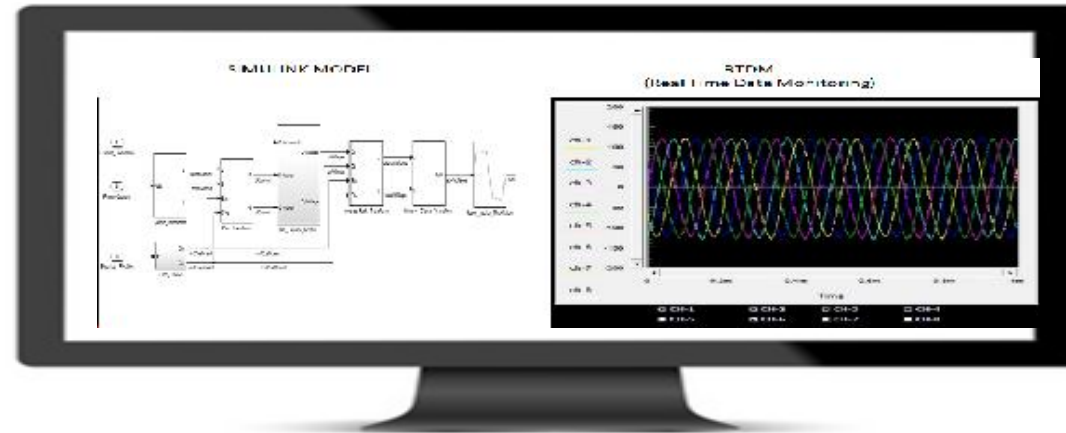


MATLAB/FPGA INTERFACE BOARD



MATLAB SIMULINK



HDL CODER



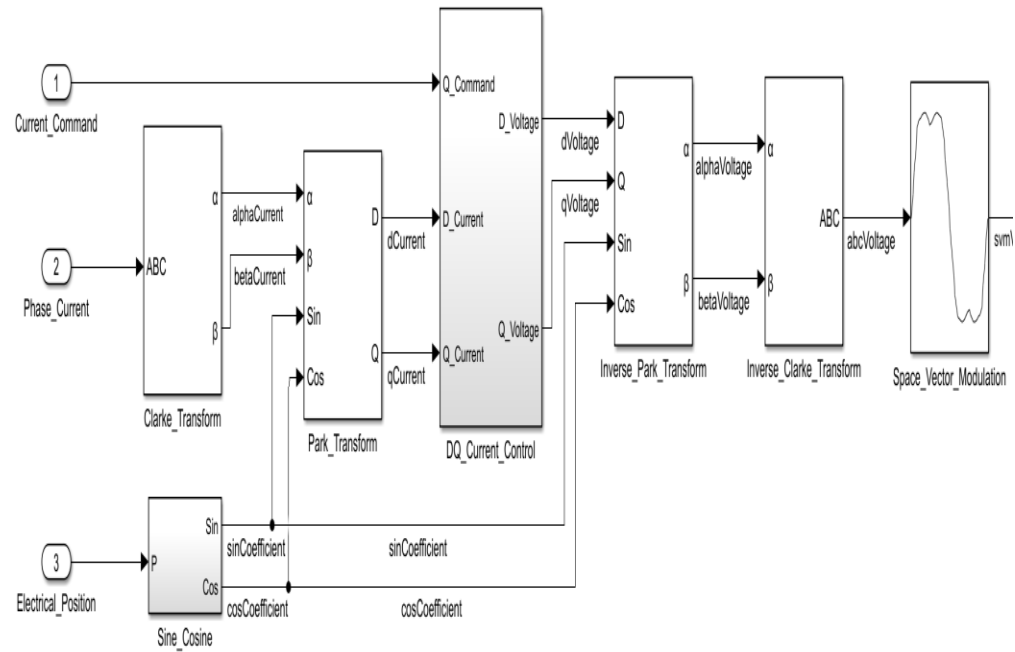
VIVADO IMPLEMENTATION



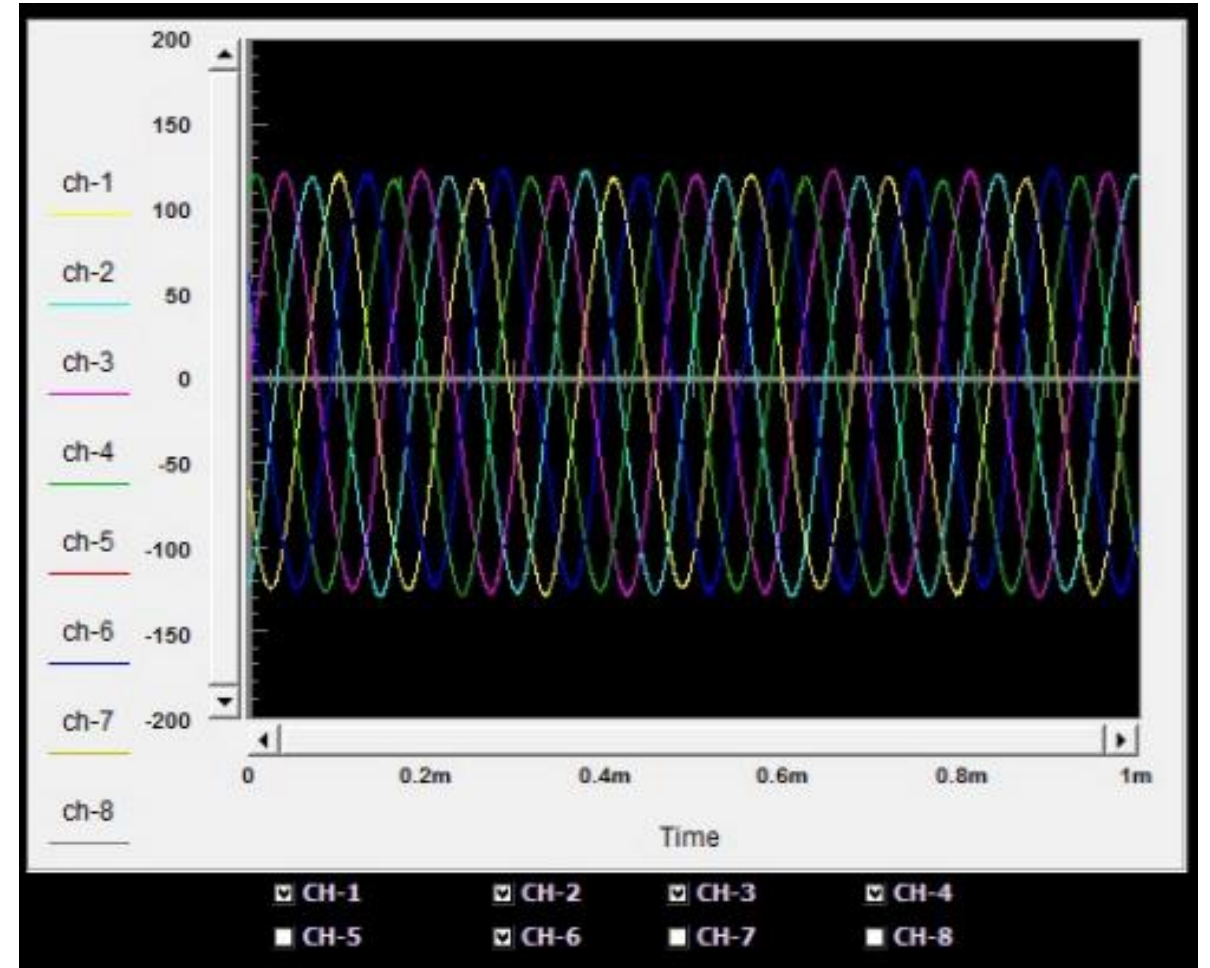
HARDWARE TESTING



SIMULINK MODEL



RTDM (Real Time Data Monitoring)



FEATERES

100 PWM PORTS

16 ADC INPUTS WITH RANGE OF -10 TO +10V AND 12BIT RESOLUTION

USB UART PROTOCOL FOR COMMUNICATION WITH PC

4 DAC OUTPUTS

20X4 LCD DISPLAY

4 PUSH SWITCHES FOR GIVING INCREMENTAL INPUT

8 DIP SWITCHES FOR ON/OFF INPUT

MATLAB/FPGA/GUI

HDL CODER FOR FPGA IMPLEMENTATION

PRE TESTED LIBRARY BLOCKS

SAMPLE AC,DC DRIVE SIMULINK MODEL

GUI FOR MONITERING AND CONTROL WITH PYTHON/VEEPRO

DATA STORING IN EXCELL SHEET FOR ANALYSIS

RUN TIME PARAMETER CHANGING AND TUNING

EXTENDED SUPPORT FOR RESEARCH